

CLAIMS:

1. A quad flat non-leaded semiconductor package device comprising:
 - a discrete semiconductor die;
 - a die pad having a first lateral surface for supporting the semiconductor die;
 - and
 - 5 - a plurality of vertical surfaces that are perpendicular to the first lateral surface, at least one bond pad having a first lateral surface and a plurality of vertical surfaces that are perpendicular to the first lateral surface;
 - at least one bond wire for connecting the discrete semiconductor die to the bond pad; and
 - 10 - an encapsulant for encapsulating the die, die pad, bond wire and bond pad having lateral and vertical outer surfaces, wherein the die pad is disposed at the perimeter of the encapsulant and partially exposed to one vertical outer surface of the encapsulant.
2. A quad flat non-leaded semiconductor package device according to claim 1,
15 wherein a vertical surface of the die pad is provided with interlocking means.
3. A quad flat non-leaded semiconductor package device according to claim 1,
wherein a vertical surface of the die pad is provided with means for interlocking the die pad vertically and laterally.
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4. A quad flat non-leaded semiconductor package device according to claim 1,
wherein a first and a second vertical surface of at least one pad is provided with interlocking means.
- 25 5. A quad flat non-leaded semiconductor package device according to claim 1,
wherein a vertical surface of a bond pad is provided with interlocking means.